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APPLICANTS

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\*\* CONTINUING DATA .....  
*None WLL*

\*\* FOREIGN APPLICATIONS .....  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>WLL</i> Examiner's Signature Initials	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 3	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 2
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TITLE  
 Method of fabricating MOS transistor using total gate silicidation process

<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing )
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